

APT-004  
 TRANSMITTER CIRCUITRY FOR SENDING  
 AND FOR DETECTING OOB SIGNALS  
 DEYRING

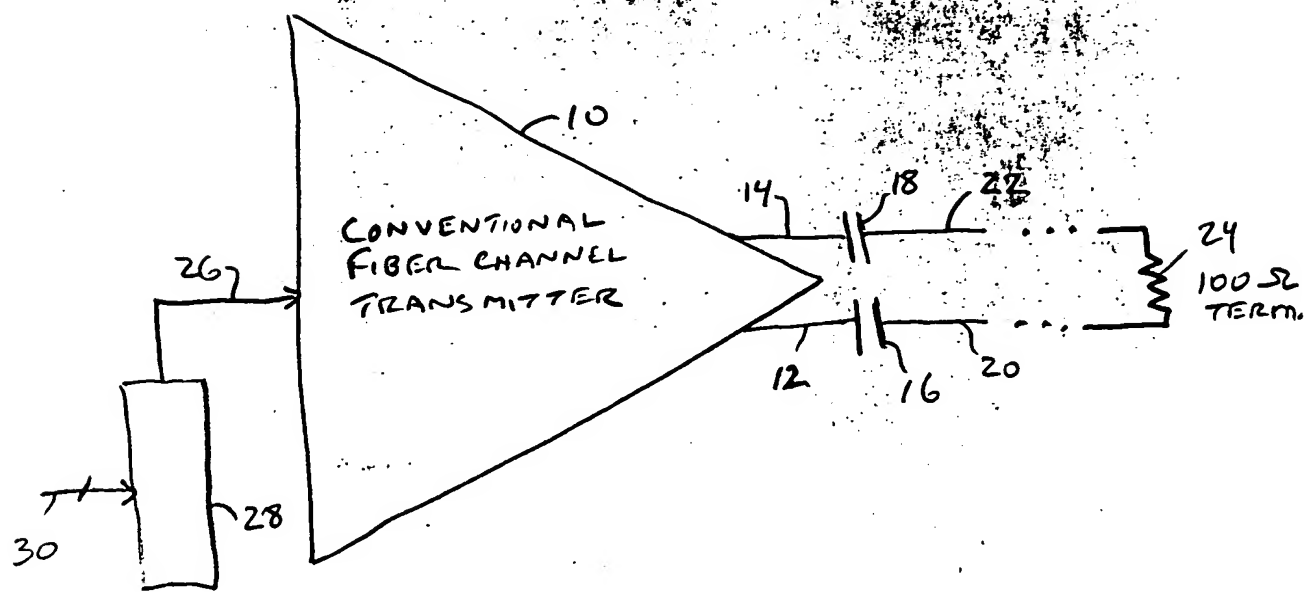


FIG. 1

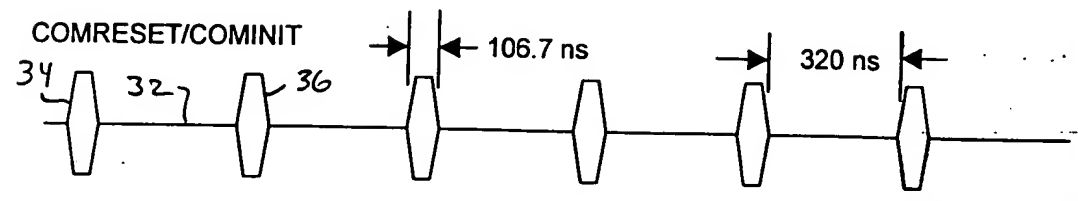


FIG. 2

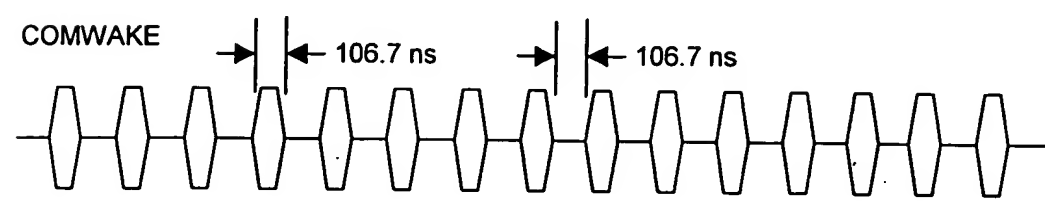


FIG. 3

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Timing diagram for a 4-phase PLL. The diagram shows four signals (A, B, C, D) and their common mode level (CM LEVEL) over time. The signals are square waves with different periods and high levels. The common mode level is shown as a dashed line. The time interval T1 is marked between the rising edges of signals A and B.

- Signal A: Period 38, High level 14.
- Signal B: Period 40, High level 12.
- Signal C: Period 42, High level 22.
- Signal D: Period 44, High level 20.
- Common Mode Level (CM LEVEL): Indicated by a dashed line.
- Time Interval T1: Marked between the rising edges of signals A and B.

FIG. 4

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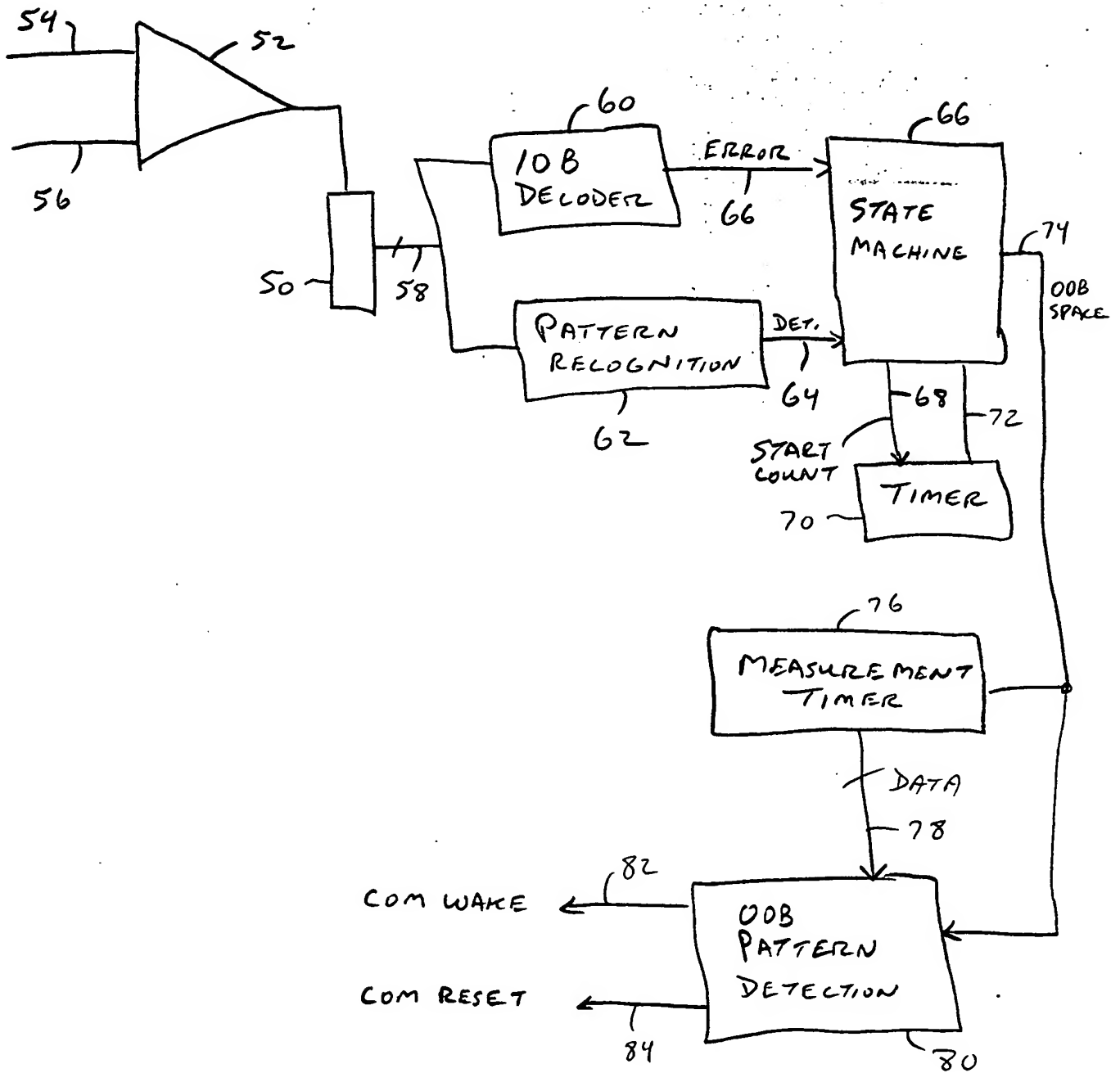


FIG. 5

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FIG. 6